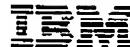IBM-POU920020032US1 Ex'nr J.P.Trimmings,
10/043486   - 7 -   IBM

C: Remarks:

The undersigned received a communication from Examiner
Trimmings on June 1, 2005 and which confirmed the allowability of
the claims after the suggested change in Claim 8. The suggestion
of the prior amendment that language relating to "introducing
said data into an inaccessible latch in a stuck-at fault LSSD
chain"..and then to trigger a change in state of at least one of
the memory units in the shift register scan path that is
detectable at the output of the shift register scan path for the
purpose of locating stuck-at fault bits in said stuck-at fault
LSSD chain has been introduced and is retained into each
independent claim. This is acknowledged in the art.

It is respectfully submitted that the application should be in
final condition for allowance which is respectfully requested.

RESPECTFULLY SUBMITTED
(For the inventors)

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